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APPLICATION

FOR

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TITLE: COMMUNICATING INFORMATION FROM AN
IMAGING DEVICE TO A PROCESSOR-BASED
SYSTEM

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COMMUNICATING INFORMATION FROM AN
IMAGING DEVICE TO A PROCESSOR-BASED SYSTEM

Background

This invention relates generally to processor-based imaging devices such as digital cameras.

Digital cameras may be tethered to processor-based systems. In one example, the tether may be a cable in accordance with the Universal Serial Bus (USB) standard. Video frames captured in the digital camera may be provided over the bus to the processor-based system. In the processor-based system, the frames may be stored or otherwise manipulated or analyzed.

A number of motion detection systems are utilized with processor-based systems. Motion detection systems may be utilized to control video capture devices for surveillance purposes as one example. As another example, video cameras may be controlled in response to motion to transmit video for incorporation into web sites through so-called "web cams".

Generally, motion detection systems for digital still or video cameras utilize a motion detector that may be an infrared sensor. The operation of the camera may be controlled by the detection of motion to avoid unnecessarily capturing video when no action is occurring. However, there is a continuing need for a better way to

control the operation of digital cameras that have motion detection.

Brief Description of the Drawings

Figure 1 is a schematic depiction of one embodiment of
5 the present invention;

Figure 2 is a block diagram of a video capture device
in accordance with one embodiment of the present invention;

10 Figure 3 is a block diagram of a processor-based
system coupled to a video capture device in accordance with
one embodiment of the present invention; and

Figure 4 is a flow chart for a software stored on the
processor-based device in accordance with one embodiment of
the present invention.

Detailed Description

15 Referring to Figure 1, a digital imaging device and
motion detector 10 may be coupled by a bus 12 to a
processor-based system 14. The processor-based system 14
may be a desktop computer, a laptop computer, an appliance,
a handheld device or a processor-based telephone. The bus
20 12 may be compliant with the Universal Serial Bus
specification. See Universal Serial Bus Revision 2.0
specification dated April 27, 2000 available from USB
Developers, Portland, Oregon 97221.

The digital imaging device and motion detector 10 may
25 include a digital still or video camera or scanner as two

examples. The digital imaging device and motion detector 10 includes a motion detector such as an infrared motion detector. The motion detector may be integral with or separate from the imaging device.

5 The digital imaging device and motion detector 10 captures a depiction of a captured image including a plurality of pixels that make up a digital frame. A plurality of frames of video may be captured and transmitted over the bus 12 to the processor-based system
10 14.

Information about whether or not motion was detected may be added to the video stream that is transmitted between the imaging device and motion detector 10 and the processor-based system 14. In one embodiment of the
15 present invention, streaming video may be forwarded over the bus 12 in the form of a series of packetized frames. Each frame may be formed of a plurality of digital packets. The packets may include information about the particular colors and intensities captured for one or more pixels by
20 the imaging device and motion detector 10.

A packet header, for example, may include information about the captured intensity of one or more pixels and color information. The brightness information may include a plurality of bits including a most significant bit and a
25 least significant bit. Information about whether or not motion was detected may be provided within a given packet

in place of the least significant bit of brightness information, in one embodiment. In other cases, other bits of video data may be replaced with information about whether or not motion was detected. In still other cases, 5 the video stream may accommodate an additional bit representing the motion information.

The motion bit may be provided with each frame or may be provided with a plurality of pixels making up a particular frame. As another example, the motion bit may 10 be provided as the least significant bit in the brightness data for one particular pixel of each frame. That is, the data associated with the same pixel in each frame is modified to include the motion bit.

Referring to Figure 2, the digital imaging device and 15 motion detector 10, in accordance with one embodiment of the present invention, may include an optics unit 202 coupled to a digital imaging array or imager 204. The imager 204 is coupled to a bus 214. The optics unit 202 focuses an optical image onto the focal plane of the imager 204. The image data (e.g., frames) generated by the imager 204 may be transferred to a random access memory (RAM) 206 20 (through memory controller 208) or flash memory 210 (through memory controller 212) via the bus 214. In one embodiment of the present invention, the RAM 206 is a non-volatile memory. 25

The imaging device and motion detector 10 may also include a compression unit 216 that interacts with the imager 204 to compress the size of a generated frame before storing it in a camera memory (RAM 206 and/or flash memory 210). To transfer a frame of data to the processor-based system 14, the digital imaging device and motion detector 10 may include a serial bus interface 218 to couple the memory (RAM 206 and flash memory 210) to a serial bus 12. One illustrative serial bus is the Universal Serial Bus (USB).

The digital imaging device and motion detector 10 may also include a processor 222 coupled to the bus 214 via a bus interface 224. In some embodiments, the processor 222 interacts with the imager 204 to adjust image capture characteristics.

The serial bus interface 218 packetizes the captured pixel data and forms frames made up of pixel information including intensity information. The serial bus interface 218 may substitute a bit indicative of the information received, over the bus interface 228, from the infrared motion detector 226. That is, the infrared motion detector 226 may send a signal indicating, in each frame interval, whether or not motion was detected. Conventionally, frames are generated at a frame rate of thirty frames per second for a frame interval of 1/30 second.

If motion is detected, that information may be provided by the processor 222 to the serial bus interface 218 for incorporation within the packetized video data. In one embodiment of the present invention, the least 5 significant bit of the brightness information for at least one pixel of each frame may be removed and replaced by a bit indicative of whether or not motion was detected during the interval of a particular frame.

As a result, the motion information may be transmitted 10 to the processor-based system 14 as part of the ongoing video stream. In the processor-based system 14, a decision may be made, upon detection of the motion bit, as to what action should be taken with the video stream that is being received. In one embodiment of the present invention, if 15 the motion bit indicates motion, the captured video may be stored on the processor-based system 14. If the motion bit indicates no motion, the video may be discarded. Thus, a surveillance or motion activated video storage system may be implemented.

Referring to Figure 3, the processor-based system 14 may include a processor 300 coupled to a north bridge 302. The north bridge 302 may be coupled to a display controller 306 and a system memory 304. The display controller 306 may in turn be coupled to a display 308. The display 308 20 may be a computer monitor, a television screen, or a liquid crystal display, as examples.

The north bridge 302 is also coupled to a bus 310 that is in turn coupled to the south bridge 312. The south bridge 312 may be coupled to a hub 316 that couples a hard disk drive 318. The hard disk drive 318 may store software 5 400, described hereinafter.

The south bridge 312 may also be coupled to a USB hub 314. The hub 314 in turn is coupled to the serial bus interface 218 of the digital imaging device and motion detector 10.

10 The south bridge 312 also couples a bus 320 that is connected to a serial input/output (SIO) device 322 and a basic input/output system (BIOS) memory 328. In one embodiment of the present invention, the SIO device 322 is coupled to a modem 326 for Internet access. In this way, 15 the information may be provided from the digital imaging device and motion detector 10 to the processor-based system 14 and ultimately uploaded to the Internet. In addition, the SIO device 322 may be coupled to an input/output device 324 such as a mouse, a keyboard, a touch screen or the 20 like.

The motion detection software 400, shown in Figure 4 and stored on the hard disk drive 318 in one embodiment of the present invention, may initially detect whether motion has been identified as indicated in diamond 402. This may 25 be accomplished by depacketizing the packetized frames received from the digital imaging device and motion

detector 10. In particular, the bit indicative of motion, that has been incorporated into the video data stream, may be located and detected. If the bit indicates that motion was detected, the system 14 may enter the capture mode as indicated in block 404. In a capture mode, the particular video frame may actually be stored, as one example, on the processor-based system 14, for example on the hard disk drive 318.

After entering the capture mode and storing a particular frame, a check at diamond 406 determines whether motion is still detected. If so, the flow iterates and another frame is captured. Otherwise, the flow ends.

The packetized data from the digital video imaging device and motion detector 10 may be depacketized and displayed line by line on the processor-based system 14. The depacketization may be accomplished by a video driver associated with the processor-based system 14. That driver may be dedicated to handling the video from the device 10. In one embodiment of the present invention, the header for a particular pixel is decoded to obtain the motion bit. In other embodiments, the motion bit may be associated in a variety of packet headers or payload locations for each frame.

While the present invention has been described with respect to a limited number of embodiments, those skilled in the art will appreciate numerous modifications and

variations therefrom. It is intended that the appended claims cover all such modifications and variations as fall within the true spirit and scope of this present invention.

What is claimed is: